

A New Approach for the Application of Single Phase Transformerless Inverters with Leakage Current Elimination in PV Systems

G RajaSekhar,

¹Department of Electrical and Electronics Engineering,
Koneru Lakshmaiah Education Foundation, Vaddeswaram,
Guntur, Andhra Pradesh, India 522502.
rsgg73@kluniversity.in

Abstract

The transformerless photovoltaic (PV) inverter topology is suggested in this paper to lower leakage current.

Multilevel inverters can employ either sine waves or modified sine waves and are a source of high power that is frequently used in industrial applications. The topology benefits from a straightforward structure, light weight, and improved efficiency.

However, the parasitic capacitance that forms between the photovoltaic (PV) module and the ground provides a conduit for leakage current to pass. Without introducing any additional components, a modulation technique has a considerable influence on reducing leakage current.

In order to lower leakage current in a transformerless cascaded multilevel inverter for photovoltaic (PV) systems, a hybrid multicarrier pulse width modulation (H-MCPWM) technique is proposed in this study.

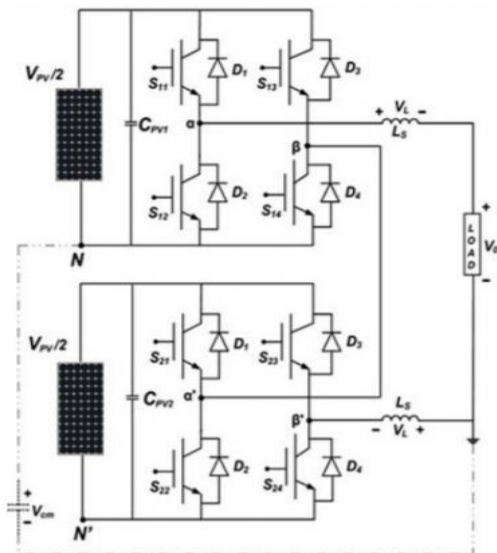
Low leakage current is guaranteed by the suggested hybrid

INTRODUCTION

Due to its high installation cost, the photovoltaic (PV) device's [1] combined electricity age is comparatively low when compared to other commonplace power assets [2]. The PV framework's productivity and cost reduction have generated more notable study interest [3]. Eliminating the transformer needed to produce the PV inverter is one way to lower the cost of the PV system architecture [4].

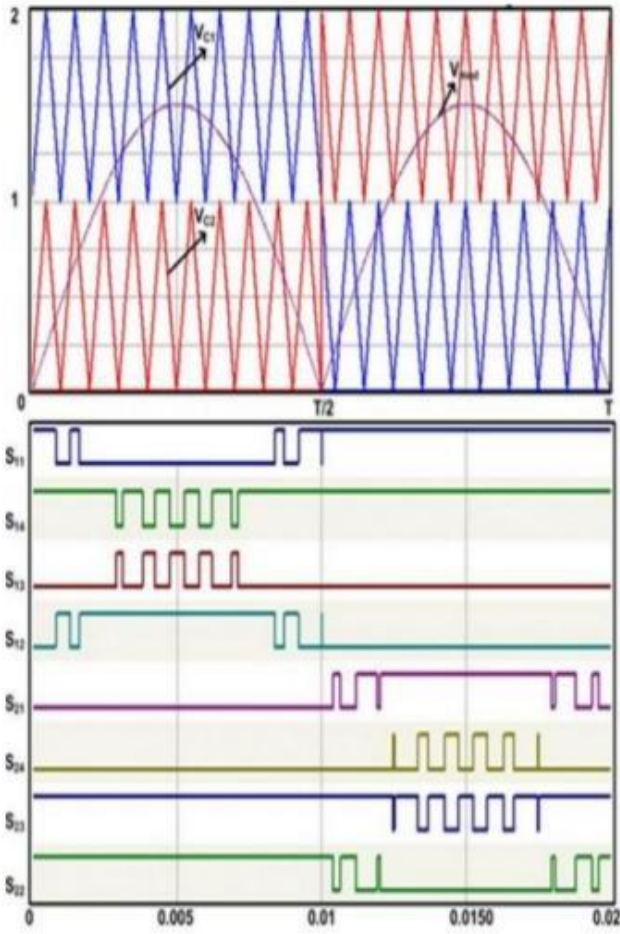
I. CASCADED MULTILEVEL INVERTER AND HYBRID MULTI CARRIER MODULATION SCHEME

FOR CONSTANT COMMON MODE VOLTAGE

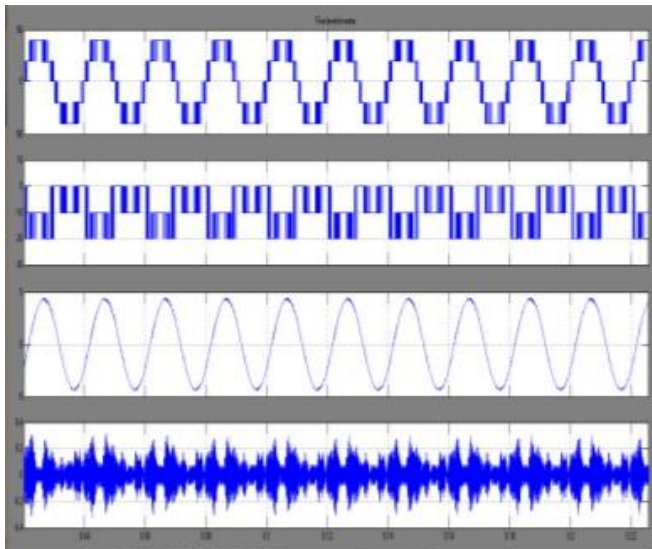


Switching pattern of the proposed H-MCPWM technique for the five level cascaded multilevel inverter

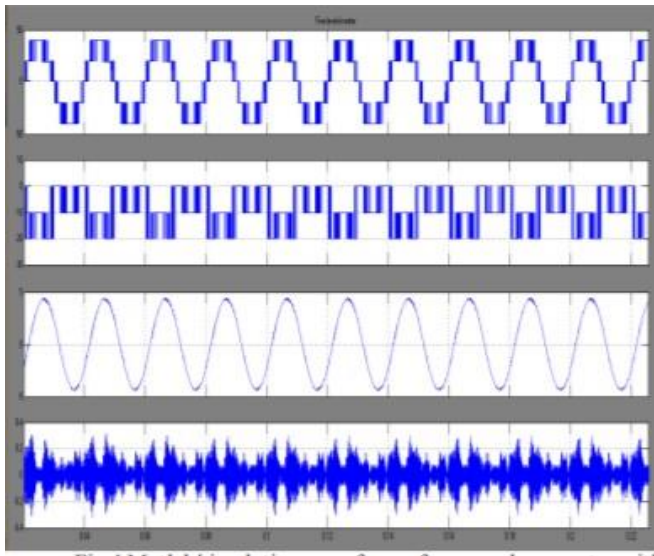
3) When each the service signals, V_{c1} then V_{c2} [5], are smaller than the reference signal V_{ref} , after the switches, S_{13} , S_{12} , S_{23} , or S_{22} , are turned ON and the complimentary switches [6], S_{11} , S_{14} , S_{21} , yet S_{24} , are grew to become OFF. In its situation $V_{\alpha N} = 0$, $V_{\beta N} = V_P V/2$, or the output voltage is $V_{\alpha \beta} = -V_P V/2$ [7].



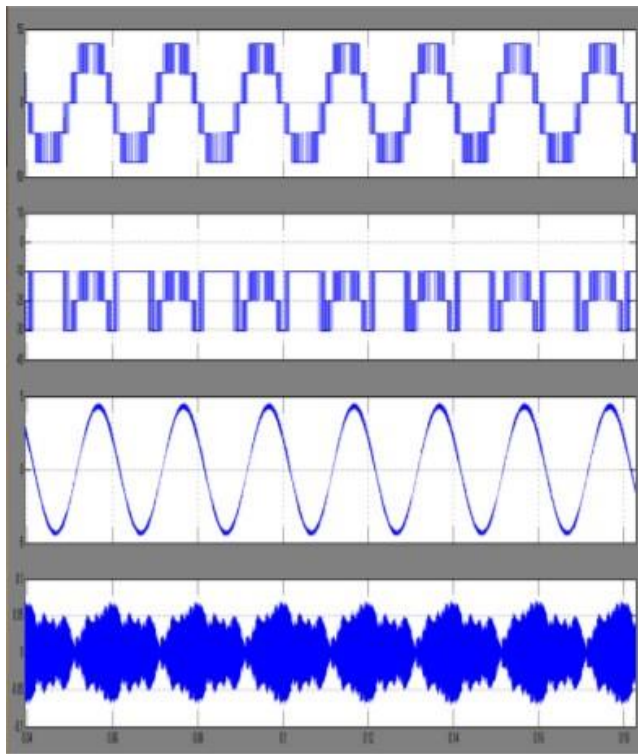
III. MATLAB/SIMULATION RESULTS



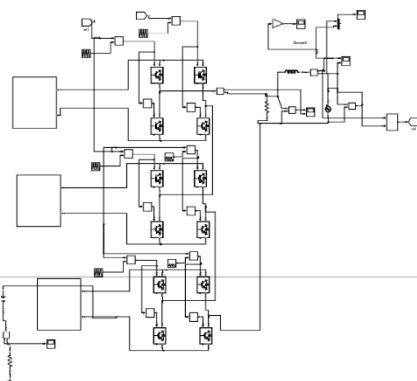
Wave form of proposed converter with in-phasedeposition



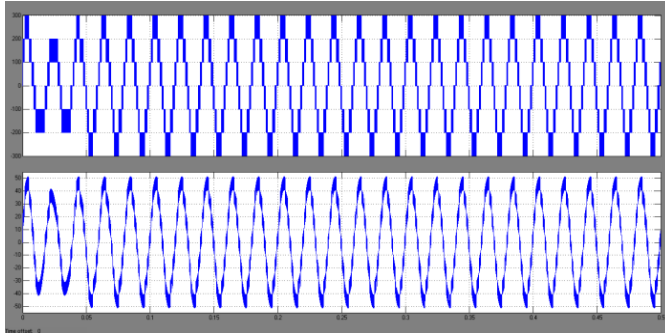
Wave form of proposed converter with out -phasedeposition



Wave form of proposed converter with multi carrierdeposition[8]



Model of proposed Seven Level Converter



Output Waveforms of H-MCPWM seven level inverter

Conclusion

This research suggests using a transformer-less cascaded multilevel inverter using the H-MCPWM technique for PV systems. It has been demonstrated that the proposed modulation technique has far less leakage than the two- and three-level inverters do today. It is also praised for offering less overall musical distortion when compared to typical modulation techniques than the proposed H-MCPWM. It utilizes a total of two carrier alerts to drive a five-level inverter output that is otherwise IV among mean multicarrier modulation ways, followed by a lowered wave carrier new or even after teaching the characteristics of an induction motor.

References

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